

FIG. 1
 PRIOR ART

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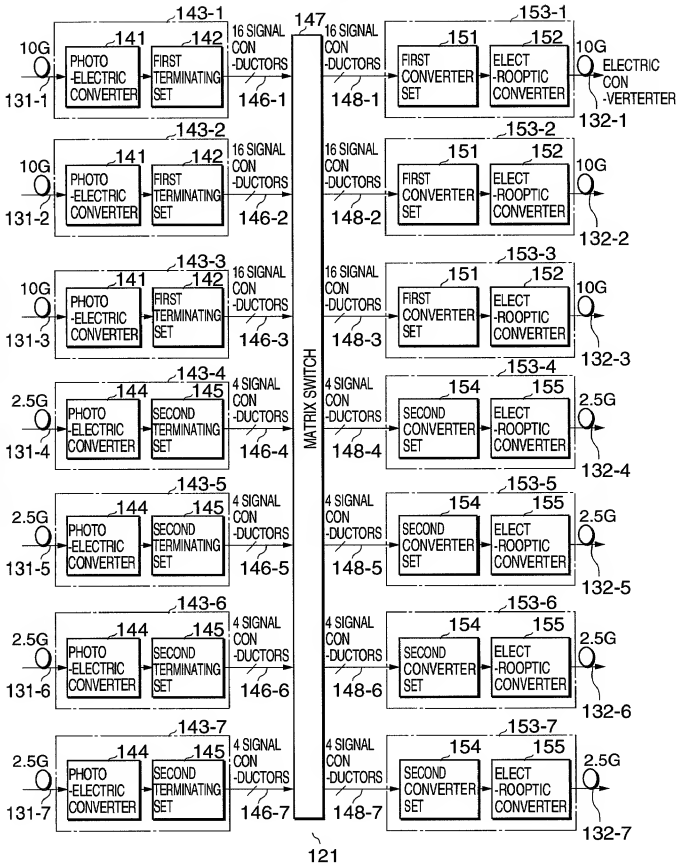


FIG. 2
PRIOR ART

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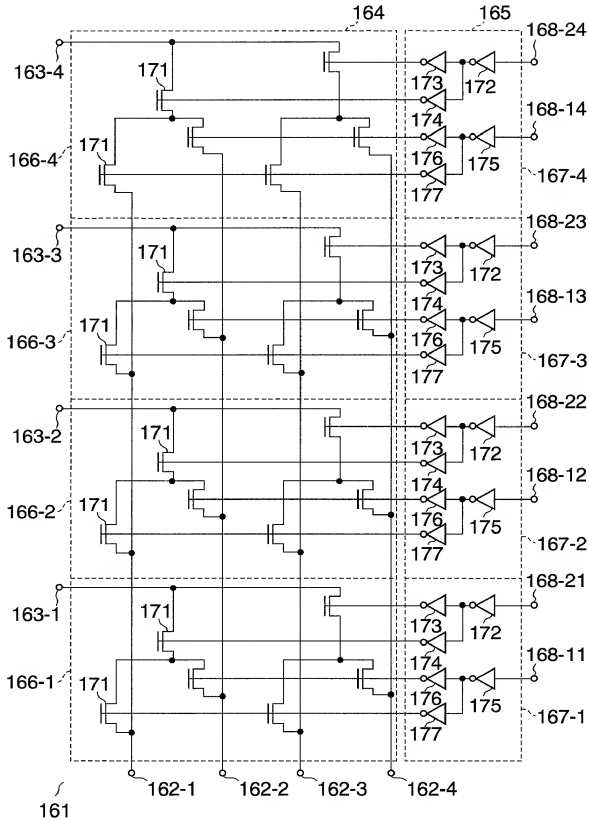


FIG. 3
PRIOR ART

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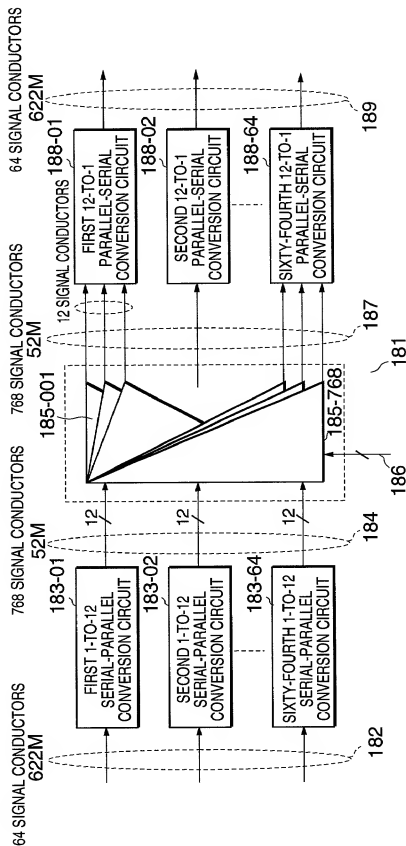


FIG. 4
 PRIOR ART

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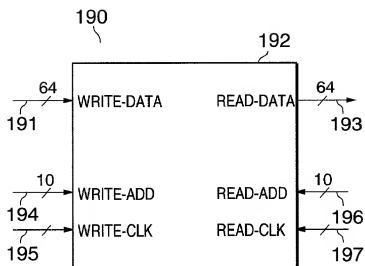


FIG. 5
PRIOR ART

09849332.050704

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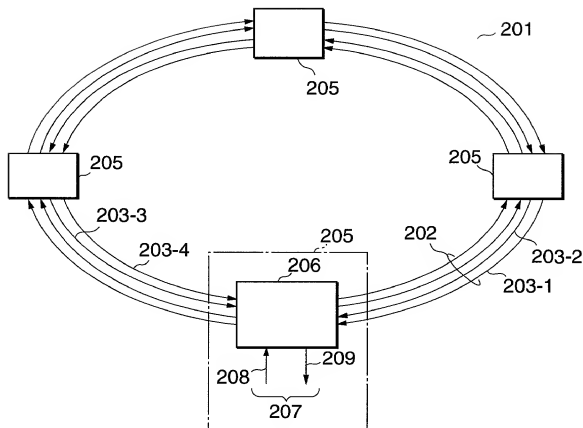


FIG. 6

03840352.050704

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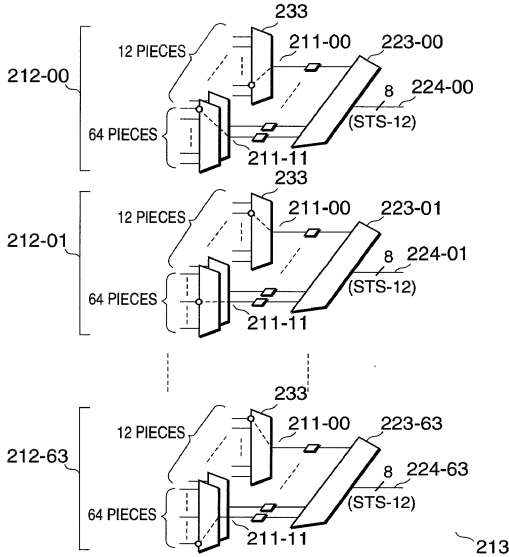


FIG. 7

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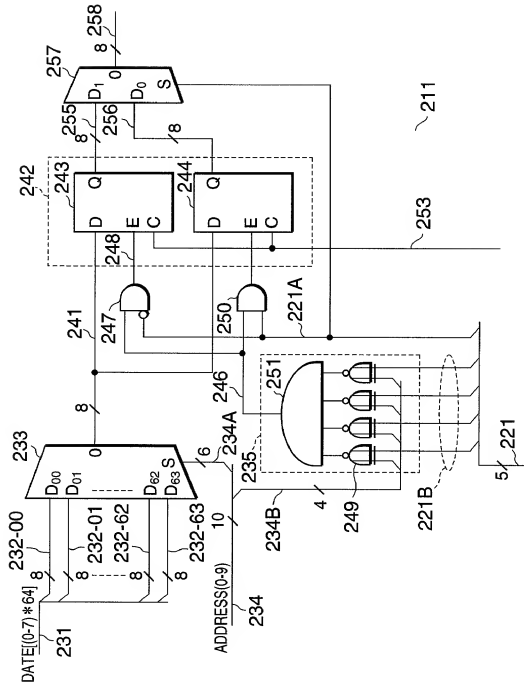


FIG. 8



FIG. 9

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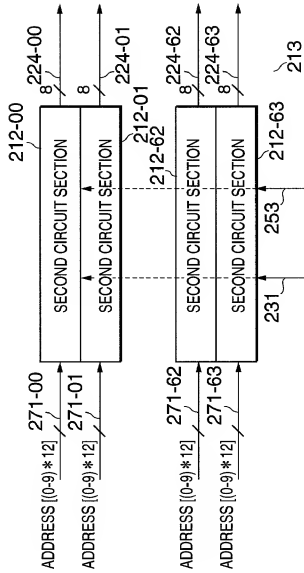


FIG. 10

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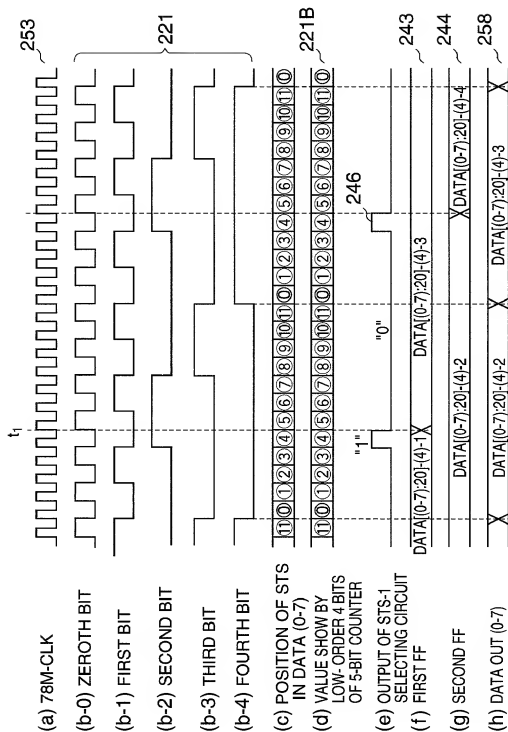


FIG. 11

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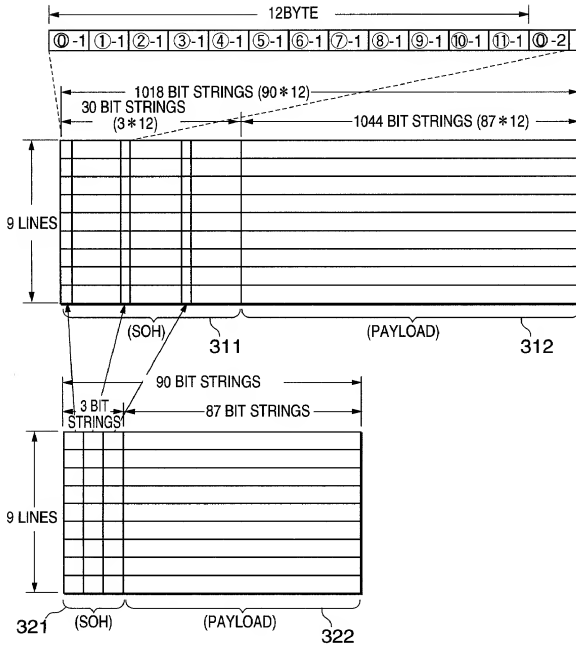


FIG. 12

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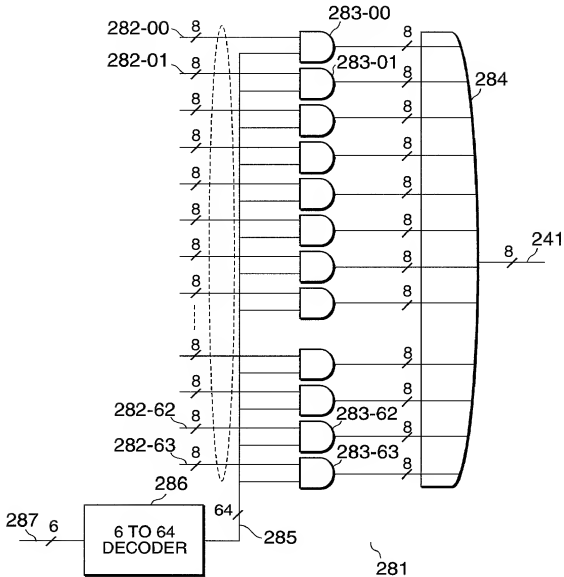


FIG. 13

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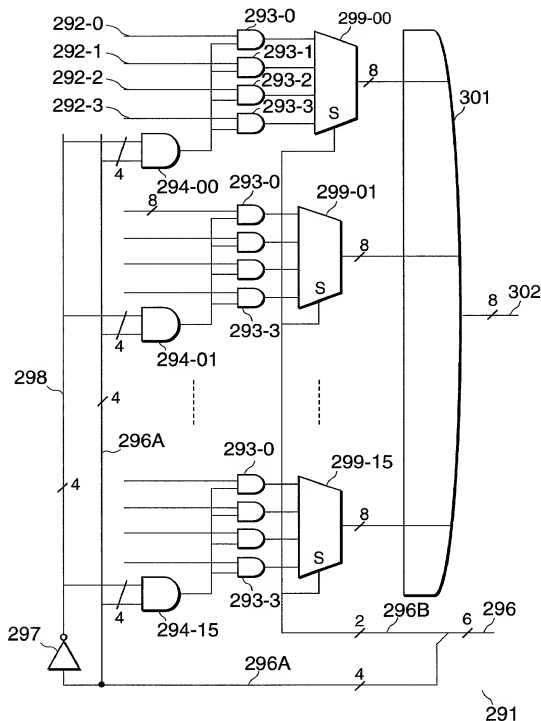


FIG. 14

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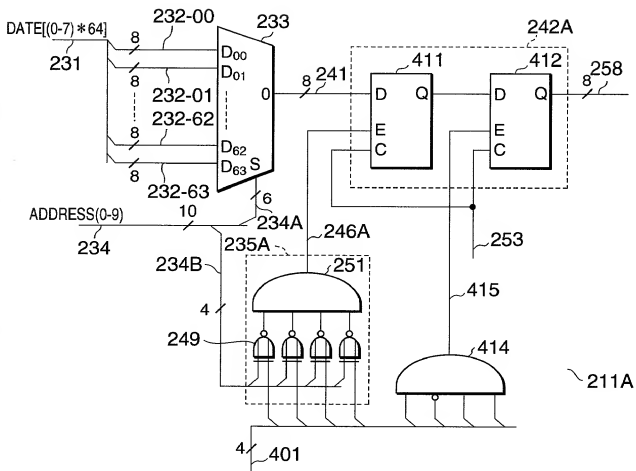


FIG. 15

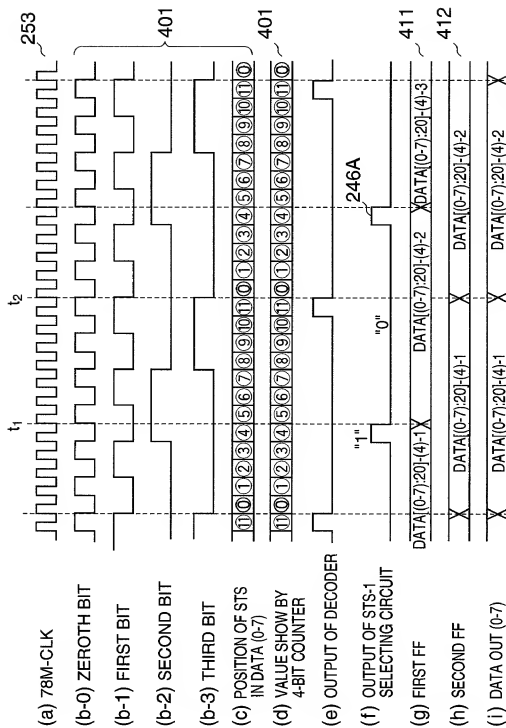


FIG. 16

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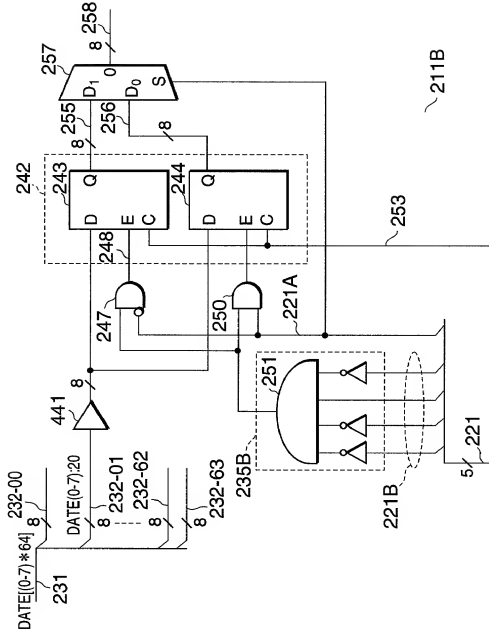


FIG. 17

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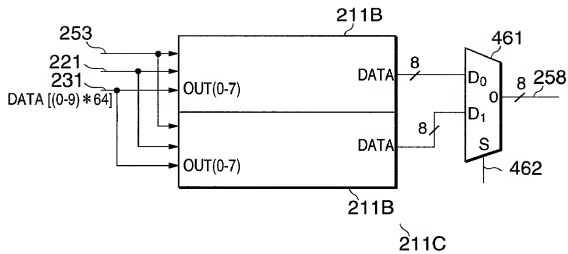


FIG. 18

0849357 050701
 102050 2526160

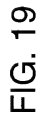


FIG. 19

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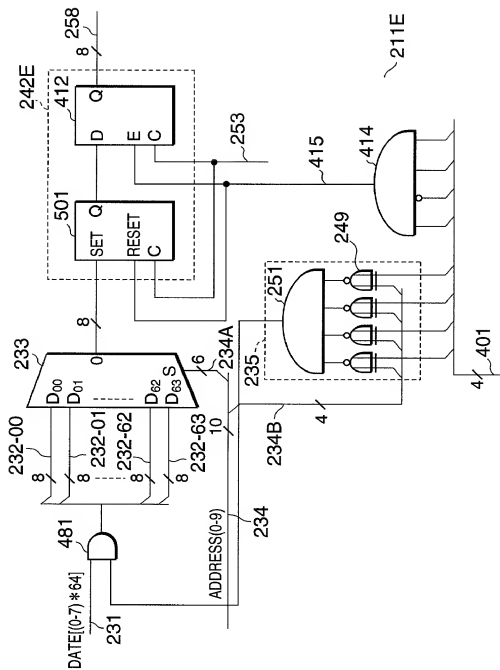


FIG. 20

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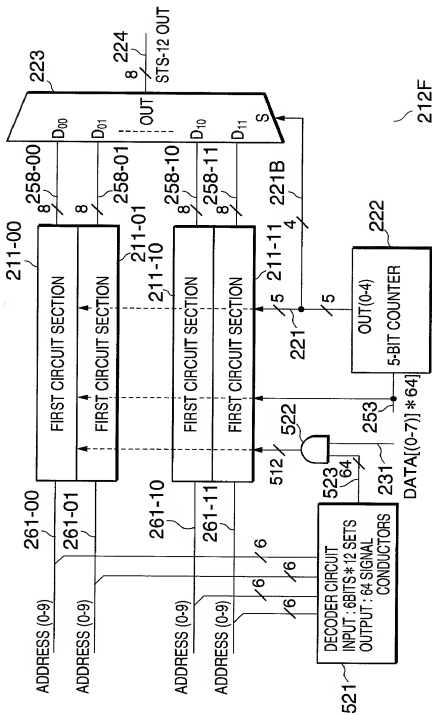


FIG. 21

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	SYSTEM SHOWN IN FIG. 8	SYSTEM SHOWN IN FIG. 9	SYNTHESIS BY MINIMIZING SCALE BY LOGICAL SYNTHESIS
POWER CONSUMPTION (mW)	120	180	934
CIRCUIT SCALE (M GATE)	0.94	1.6	1.1

FIG. 22

	SYSTEM SHOWN IN FIG. 8	SYSTEM SHOWN IN FIG. 9	SYNTHESIS BY MINIMIZING SCALE BY LOGICAL SYNTHESIS
POWER CONSUMPTION (mW)	20	30	156
CIRCUIT SCALE (M GATE)	1.2	1.6	1.3

FIG. 23

	SYSTEM SHOWN IN FIG. 8	SYSTEM SHOWN IN FIG. 9	SYNTHESIS BY MINIMIZING SCALE BY LOGICAL SYNTHESIS
POWER CONSUMPTION (mW)	120	180	187
CIRCUIT SCALE (M GATE)	1.0	1.4	1.2

FIG. 24